

| L Number | Hits | Search Text | DB | Time stamp |
|----------|-------|--|-------|------------------|
| - | 19620 | microcontroller | USPAT | 2003/09/26 17:12 |
| - | 998 | microcontroller and mux | USPAT | 2003/09/26 17:02 |
| - | 0 | microcontroller and mux and internal and external and peripheral | USPAT | 2003/09/26 17:03 |
| - | 355 | microcontroller and mux and internal and external and peripheral | USPAT | 2003/09/26 17:10 |
| - | 0 | (microcontroller and mux and internal and external and peripheral) and structurable | USPAT | 2003/09/26 17:05 |
| - | 0 | (microcontroller and mux and internal and external and peripheral) and structural | USPAT | 2003/09/26 17:04 |
| - | 0 | (microcontroller and mux and internal and external and peripheral) and intelligent and interface | USPAT | 2003/09/26 17:05 |
| - | 83 | (microcontroller and mux and internal and external and peripheral) and intelligent and interface | USPAT | 2003/09/26 17:04 |
| - | 83 | (microcontroller and mux and internal and external and peripheral) and intelligent and interface | USPAT | 2003/09/26 17:05 |
| - | 0 | (microcontroller and mux and internal and external and peripheral) and structurable | USPAT | 2003/09/26 17:05 |
| - | 0 | (microcontroller and mux and internal and external and peripheral) and "intelligent core" and interface | USPAT | 2003/09/26 17:06 |
| - | 44 | (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core | USPAT | 2003/09/26 17:06 |
| - | 10 | (microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data) | USPAT | 2003/09/26 17:07 |
| - | 7 | ((microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)) and circuit and dispose\$1 | USPAT | 2003/09/26 17:08 |
| - | 7 | ((microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)) and circuit and dispose\$1 and multiplexer | USPAT | 2003/09/26 17:09 |
| - | 0 | ((microcontroller and mux and internal and external and peripheral) and intelligent and interface and core and (process near data)) and circuit and dispose\$1 and multiplexer and configuration | USPAT | 2003/09/26 17:09 |
| - | 165 | microcontroller and internal and external and peripheral and circuit and dispose\$1 and multiplexer and configuration | USPAT | 2003/09/26 17:11 |
| - | 55 | microcontroller and internal and external and peripheral and circuit and dispose\$1 and multiplexer and configuration and array and NAND | USPAT | 2003/10/09 17:50 |
| - | 1 | microcontroller and DNF | USPAT | 2003/09/26 17:12 |
| - | 46 | microcontroller and internal and external and peripheral and circuit and dispose\$1 and multiplexer and configuration and array and NAND and (state near machine) | USPAT | 2003/10/08 18:29 |
| - | 11 | microcontroller and internal and external and peripheral and circuit and dispose\$1 and multiplexer and configuration and array and NAND and (state near machine) and fuse | USPAT | 2003/09/26 17:19 |
| - | 9 | microcontroller and internal and external and peripheral and circuit and dispose\$1 and multiplexer and configuration and array and NAND and (state near machine) and fuse and reverse | USPAT | 2003/10/08 18:29 |
| - | 108 | (712/ .*) and program-controlled | USPAT | 2003/10/08 18:49 |
| - | 2 | (712/ .*) and program-controlled and (process near instruction\$1) | USPAT | 2003/10/08 18:50 |
| - | 1 | (712/ .*) and program-controlled and (process near instruction\$1) and internal and peripheral | USPAT | 2003/10/08 18:51 |

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|---|-------|---|---|------------------|
| - | 11 | (712/ .*) and program-controlled and intelligent | USPAT | 2003/10/08 18:54 |
| - | 1 | (712/ .*) and program-controlled and intelligent and peripheral | USPAT | 2003/10/08 18:52 |
| - | 2 | (712/ .*) and program-controlled and intelligent and circuit and dispose\$1 and multiplexer and configuration | USPAT | 2003/10/08 18:55 |
| - | 2 | (712/ .*) and program-controlled and intelligent and circuit and dispose\$1 and multiplexer and configuration | USPAT | 2003/10/08 18:55 |
| - | 2 | 20020013630.pn. | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2003/10/09 17:53 |
| - | 56447 | 712/ .* | USPAT | 2003/10/14 14:18 |
| - | 37 | (712/ .*) and fuse and "anti-fuse" | USPAT | 2003/10/14 14:19 |